Day: Wednesday





## PALM INTRANET

## **Inventor Name Search Result**

Your Search was:

Last Name = GROEN First Name = ERIC

Application#	Patent#	Status	Date Filed	Title	Inventor Name		
09152869	61218 <u>00</u>	150	09/14/1998	IMPEDANCE MATCHED, VOLTAGE-MODE H-BRIDGE WRITE DRIVERS	GROEN, ERIC		
09630494	62852 <u>21</u>	150	08/02/2000	IMPEDANCE-MATCHED, VOLTAGE-MODE H-BRIDGE WRITE DRIVERS	GROEN, ERIC		
<u>09706904</u>	6437599	150	11/06/2000	PROGRAMMABLE LINE DRIVER	GROEN, ERIC		
11114807	7167 <u>410</u>	150	04/26/2005	MEMORY SYSTEM AND MEMORY DEVICE HAVING A SERIAL INTERFACE	GROEN, ERIC		
11178958	Not Issued	30	07/11/2005	Method of encoding and synchronizing a serial interface	GROEN, ERIC		
10097689	6683502	150	03/12/2002	PROCESS COMPENSATED PHASE LOCKED LOOP	GROEN, ERIC D.		
10407281	688 <u>2224</u>	150	04/03/2003	SELF-BIASING FOR COMMON GATE AMPLIFIER	GROEN, ERIC D.		
10659803	Not Issued	30		Analog front-end having built-in equalization and applications thereof	GROEN, ERIC D.		
10659819	710 <u>9809</u>	150	09/11/2003	METHOD AND CIRCUIT FOR REDUCING VCO NOISE	GROEN, ERIC D.		
<u>10659916</u>	7047457	150	09/11/2003	TESTING OF A MULTI- GIGABIT TRANSCEIVER	GROEN, ERIC D.		
10659971	Not Issued	93	09/11/2003	RECEIVER TERMINATION NETWORK AND APPLICATION THEREOF	GROEN, ERIC D.		
10659973	7015838	150		PROGRAMMABLE SERIALIZING DATA PATH	GROEN, ERIC D.		
10659978	6956442	150		RING OSCILLATOR WITH PEAKING STAGES	GROEN, ERIC D.		
10660062	<u>6</u> 97 <u>5</u> 1 <u>32</u>	150	09/11/2003	DAC BASED DRIVER WITH	GROEN, ERIC D.		

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	-				SELECTABLE PRE-EMPHASIS SIGNAL LEVELS	
	10660159	69761 <u>02</u>	150	09/11/2003	INTEGRATED CIRCUIT WITH AUTO NEGOTIATION	GROEN, ERIC D.
	10660190	Not Issued	80	09/11/2003	Programmable on chip regulators with bypass	GROEN, ERIC D.
	10660254	Not Issued	71		MGT/FPGA clock management system	GROEN, ERIC D.
	10660448	6870390	150	09/11/2003	TX LINE DRIVER WITH COMMON MODE IDLE STATE AND SELECTABLE SLEW RATES	GROEN, ERIC D.
	10661016	Not Issued	71	09/11/2003	Programmable logic device including programmable multigigabit transceivers	GROEN, ERIC D.
	10812549	7196545	150	03/29/2004	HIGH FREQUENCY LATCH	GROEN, ERIC D.
	10990047	7142014	150	11/16/2004	HIGH FREQUENCY XOR WITH PEAKED LOAD STAGE	GROEN, ERIC D.
	11218962	7227375	150	09/01/2005	DAC BASED DRIVER WITH SELECTABLE PRE-EMPHASIS SIGNAL LEVELS	GROEN, ERIC D.
	11326548	7116251	150	01/04/2006	PROGRAMMABLE SERIALIZING DATA PATH	GROEN, ERIC D.
	09968471	6507220	150	09/28/2001	CORRECTION OF DUTY- CYCLE DISTORTION IN COMMUNICATIONS AND OTHER CIRCUITS	GROEN, ERIC DOUGLAS
	10294254	6690202	150	11/13/2002	CORRECTION OF DUTY- CYCLE DISTORTION IN COMMUNICATIONS AND OTHER CIRCUITS	GROEN, ERIC DOUGLAS

Inventor Search Completed: No Records to Display.

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